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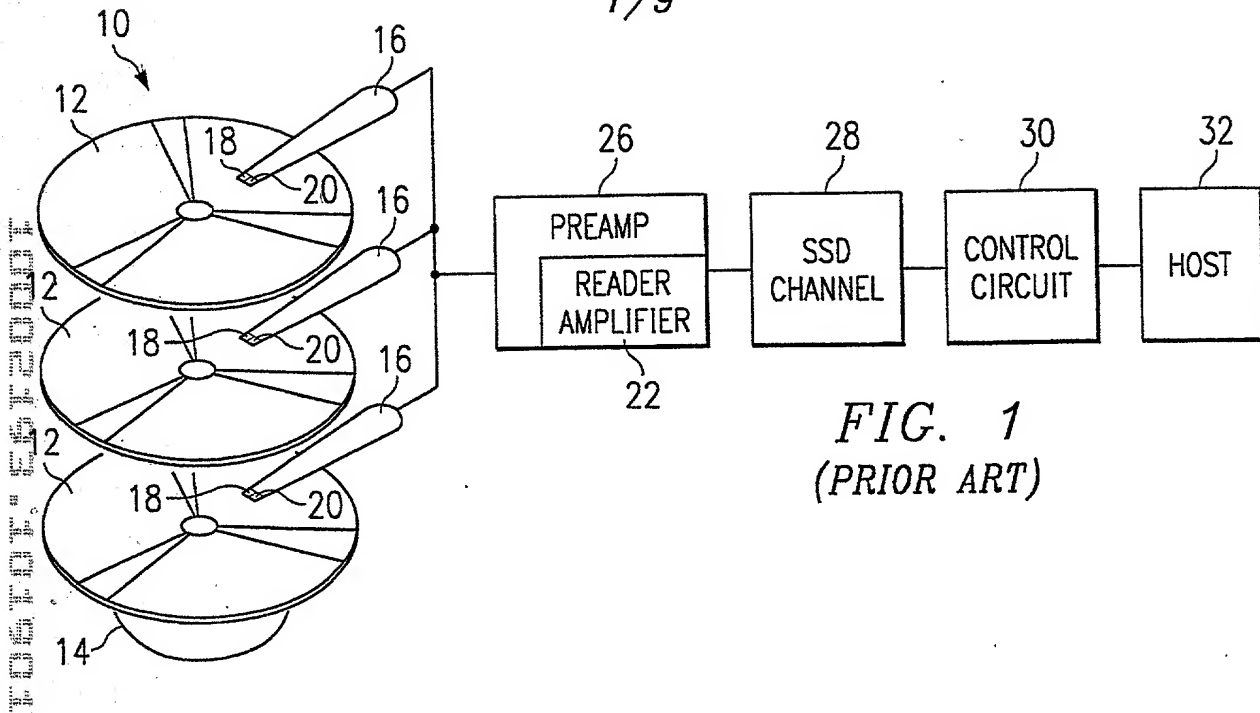


Figure 2 is a schematic diagram of a circuit.

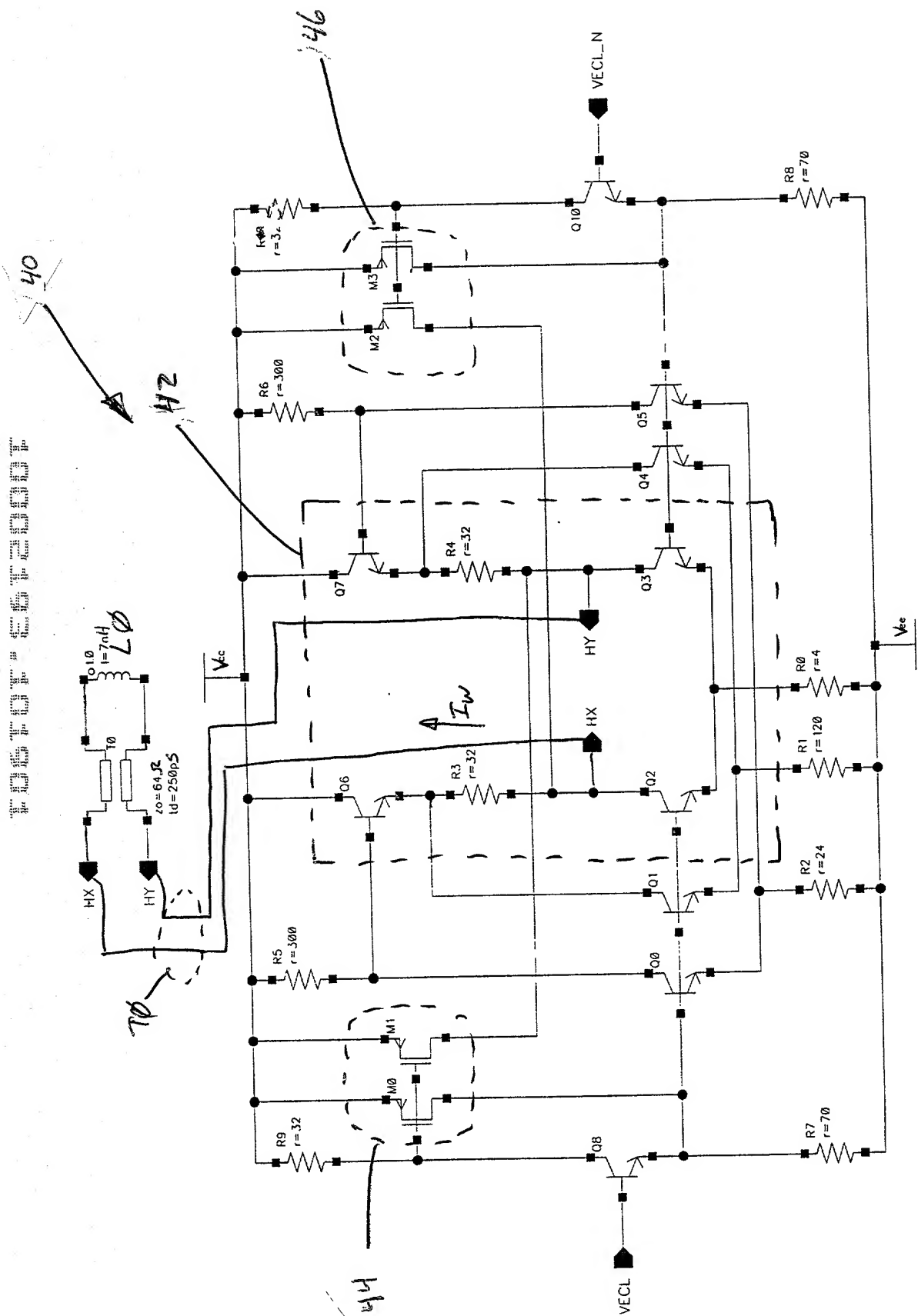


FIGURE 2



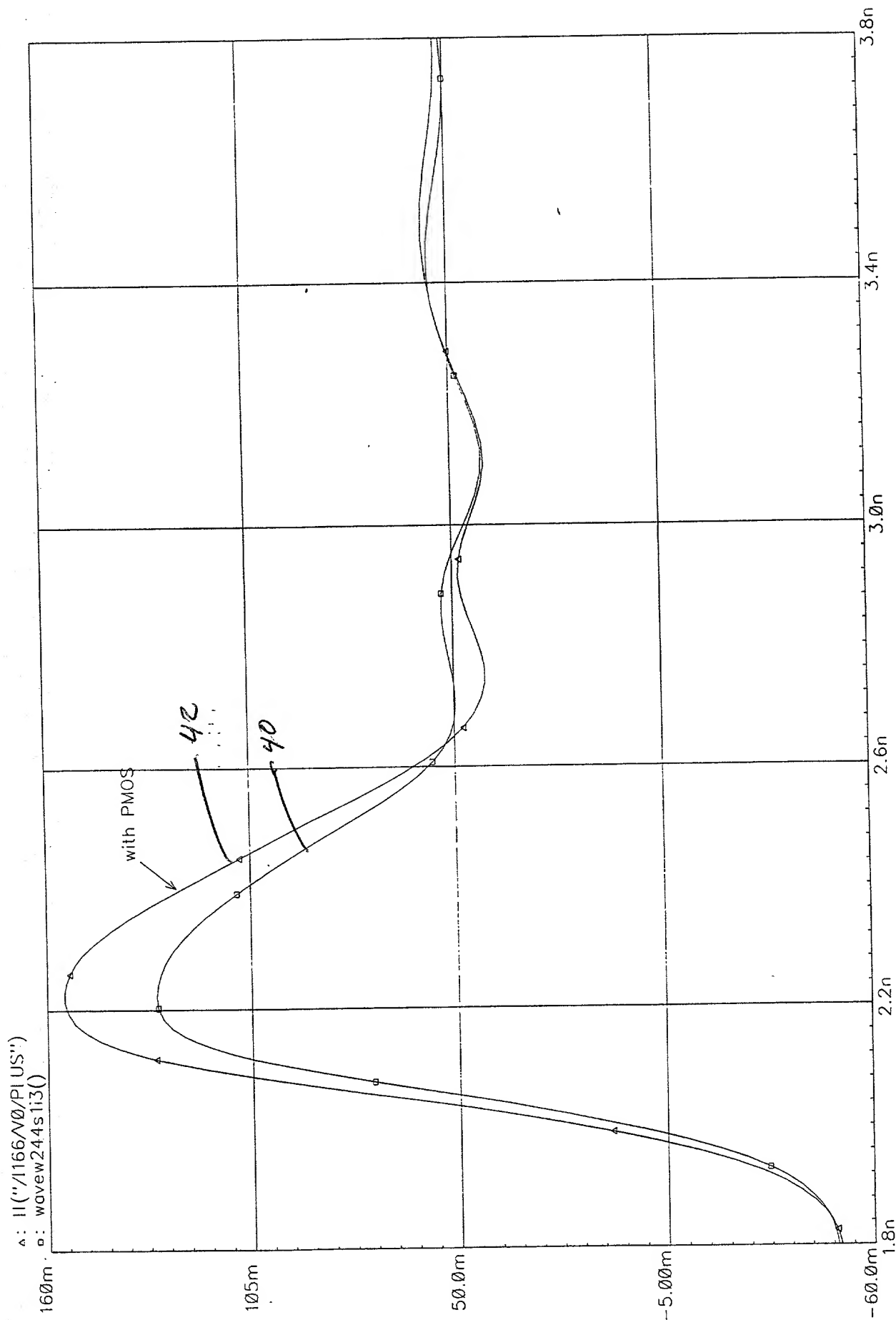


FIGURE 4